SPECIFICATION

| MODULE | NO.:16 | 60160-03 | |
|----------------|--------|-------------------|----------|
| | Rev: | <u>01</u> | |
| | Date: | <u>2010-09-15</u> | |
| APPROVED | | CHECKED | PREPARED |
| | | | |
| Customer Annua | 1. | | |
| Customer Appro | var. | ☐ Accept☐ Reject | |
| | | Comment: | : |
| | | Approved | by: |
| | | 1 | |
| | | | |
| | | | |
| EL: | | | |
| AX: | | | |

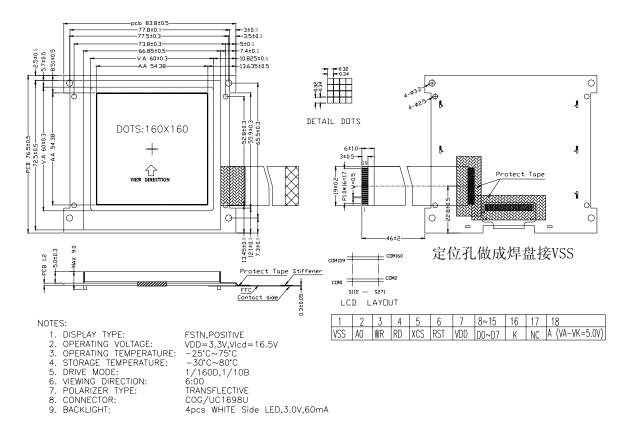
History of Version

| Date | Ver | Description | PREPARED | Checked |
|------------|-----|-------------|----------|---------|
| 2010/09/15 | 01 | New sample | XQC | |
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1.DIMENSIONAL OUTLINE



2.FUNCTIONS & FEATURES

2-1. Format : 160X160 Dots

2-2. LCD mode : FSTN, Positive Transflective Mode

2-3. Viewing direction : 6 o'clock

2-4. Driving scheme : 1/160 Duty, 1/10 Bias

2-5. Low power operation : Power supply voltage range (V_{DD}): 2.8~3.3V

2-6. VLCD adjustable for best contrast : LCD driving voltage (V_{OP}): 16.5V

2-7. Operating temperature $: -25^{\circ}\text{C} \sim 75^{\circ}\text{C}$ 2-8. Storage temperature $: -30^{\circ}\text{C} \sim 80^{\circ}\text{C}$

2-9. Backlight : 4pcs WHITE side LED (If=60mA)

3.MECHANICAL SPECIFICATIONS

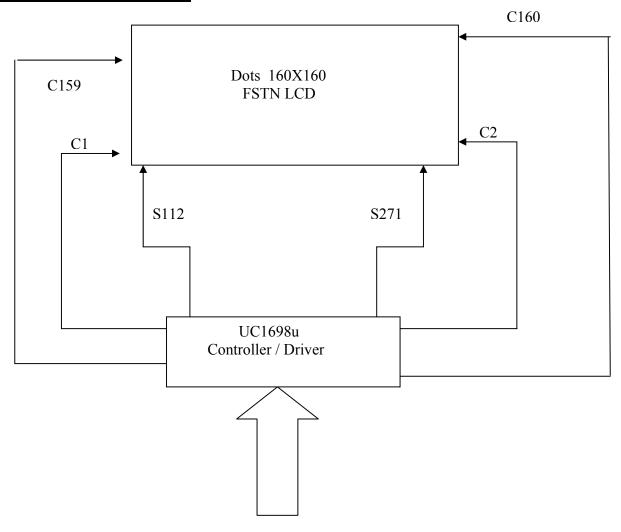
 3-1. Module size
 : 83.8mm(L)*76.5mm(W)

 3-2. Viewing area
 : 60.0mm(L)*60.0mm(W)

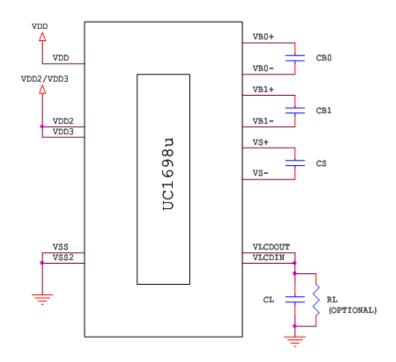
 3-3. Dot pitch
 : 0.34mm(L)*0.34mm(W)

 3-4. Dot size
 : 0.32mm(L)*0.32mm(W)

4.BLOCK DIAGRAM



5.POWER SUPPLY



6. PIN DESCRIPTION

| Pin no. | Cramb of | Function |
|-----------|-------------|--|
| PIII IIO. | Symbol | |
| 1 | $ m V_{SS}$ | Ground |
| 2 | A0 | COMMAND and DATA register select signal |
| 3 | WR | In 8080 mode, use as Write enable input."L" active |
| 4 | RD | In 8080 mode, use as Read enable input."L" active |
| 5 | CS | This is the chip select signal. "L" active |
| 6 | RST | Reset input pin, When RES is "L", initialization is executed |
| 7 | $V_{ m DD}$ | Supply voltage for logic circuit +3.3V |
| 8 | DB0 | |
| 9 | DB1 | |
| 10 | DB2 | |
| 11 | DB3 | |
| 12 | DB4 | Display data signal |
| 13 | DB5 | |
| 14 | DB6 | |
| 15 | DB7 | |
| 16 | BLK | Supply voltage for backlight Ground (0V) |
| 17 | NC | |
| 18 | BLA | Supply voltage for backlight +5.0V |

7.MAXIMUM ABSOUTE LIMIT (T=25°C)

| Item | Symbol | Standard value | Unit |
|--|-------------|----------------|------|
| Power supply voltage for logic | $V_{ m DD}$ | -0.3~+4.0 | V |
| Driver supply voltage for LCD (V_{DD} - V_0) | V_{LCD} | -0.3~+19.8 | V |
| Operating temperature | Topr | -25~+75 | °C |
| Storage temperature | Tstg | -30~+80 | °C |

Note: Voltage greater than above may damage the module

8.ELECTRICAL CHARACTERISTICS

8-1 DC Characteristics

| Parameter | Symbol | Min | Typ | Max | Unit |
|----------------------------|----------------------------------|--------------------|------|--------|------|
| Supply voltage for logic | V _{DD} -V _{SS} | 3.2 | 3.3 | 3.4 | V |
| Input Current | Idd | - | 1.64 | 3.28 | mA |
| Operating voltage for LCD | VLCD | - | 16.5 | - | V |
| Input voltage 'H'level | V_{IH} | $0.8V_{DD}$ | - | - | V |
| Input voltage 'L' level | VIL | - | - | 0.2Vdd | V |
| Output voltage ' H ' level | Voh | 0.8V _{DD} | - | - | V |
| Output voltage ' L ' level | Vol | - | - | 0.2Vdd | V |

POWER CONSUMPTION

 $V_{DD} = 2.7 \text{ V}$, Bias Ratio = 12, PM = 64,

 $V_{LCD} = 16.5 \text{ V}$, Line Rate = 10b, Panel Loading (PC[1:0]) = 0b,

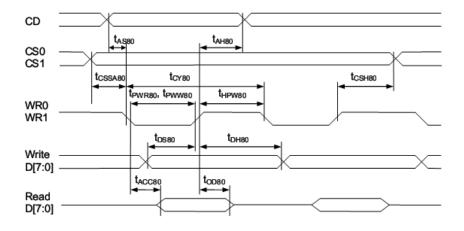
Mux Rate = 160, Bus mode = 6800, $C_L = 330$ nF, $C_B = 2.2$ μF, Temperature = 25°C, MTP=00 H,

N-line inversion = 31 lines Color Mode = 64K color mode, All HV outputs are open circuit.

| Display Pattern | Conditions | Typ. (μA) | Max. (μA) |
|-----------------|--------------------------|-----------|-----------|
| All-Pixel-OFF | Bus = idle | 1764 | 2470 |
| 2-pixel checker | Bus = idle | 2468 | 3455 |
| None | Reset (stand-by current) | <1 | 5 |

8-2 AC Characteristics

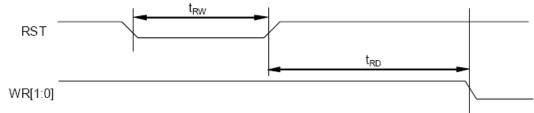
System buses Read/Write (For the 8080 Series MPU)



(2.5V \leq V_{DD} < 3.3V, Ta= -30 to +85 $^{\circ}$ C)

| Symbol | Signal | Description | Condition | Min. | Max. | Units |
|---|----------|--|----------------------------|-------------------------------|----------|-------|
| t _{AS80} t _{AH80} | CD | Address setup time Address hold time | | 0 | - | nS |
| t _{CY80} | | System cycle time 16-bit bus (read) (write) 8-bit bus (read) (write) | LC[7:6]=10b LC[7:6]=01b | 170 130 100 80 90 | 1 | nS |
| t _{PWR80} | WR1 | Pulse width 16-bit (read) 8-bit | | 85 50 | - | nS |
| t _{PWW80} | WR0 | Pulse width 16-bit (write) 8-bit | LC[7:6]=10b LC[7:6]=01b | 65 40 45 | ı | nS |
| t _{HPW80} | WR0, WR1 | High pulse width 16-bit bus (read) (write) 8-bit bus (read) (write) | LC[7:6]=10b LC[7:6]=01b | 85 65 50 40 45 | 1 | n\$ |
| t _{DS80} t _{DH80} | D0~D15 | Data setup time Data hold time | | 30 0 | 1 | nS |
| t _{ACC80} | | Read access time Output disable time | C _L = 100pF | - 15 | 60 30 | nS |
| T _{CSSA80} t _{CSH80} | CS1/CS0 | Chip select setup time | | 5 5 | | nS |

Reset Characteristics



(1.65V \leq $V_{DD}\!<\,3.3V,$ Ta= –30 to +85 $^{^{o}}\!C)$

| Symbol | Signal | Description | Condition | Min. | Max. | Units |
|-----------------|---------|-------------------------|-----------|------|------|-------|
| t _{RW} | RST | Reset low pulse width | | 3 | - | μS |
| t _{RD} | RST, WR | Reset to WR pulse delay | | 10 | - | mS |

9.CONTROL AND DISPLAY COMMAND

The following is a list of host commands supported by UC1698u C/D: 0: Control, 1: Data W/R: 0: Write Cycle, 1: Read Cycle #: Useful Data bits -: Don't Care

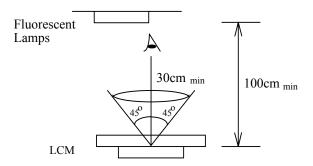
| | Command | C/D | W/R | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Actio | n | Default |
|----|---|---------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|------------------|----------------------------|---------------------------------|----------------------------------|---|------------|
| 1 | Write Data Byte | 1 | 0 | # | # | # | # | # | # | # | # | Write 1 | | N/A |
| 2 | Read Data Byte | 1 | 1 | # | # | # | # | # | # | # | # | Read 1 | • | N/A |
| | • | | | GE | MX | MY | WA | DE | ws | MD | MS | Get {Statu | | |
| 3 | Get Status & PM | 0 | 1 | Ver | | | Р | MO[6: | 0] | | | PMO, Produ | | N/A |
| | | | | Pro | duct (| Code | (8h) | PID | [1:0] | MID | [1:0] | PID, M | | |
| 4 | Set Column Address LSB | 0 | 0 | 0 | 0 | 0 | 0 | # | # | # | # | Set CA | | 0 |
| Ľ | Set Column Address MSB | 0 | 0 | 0 | 0 | 0 | 1 | 0 | # | # | # | Set CA | _ | 0 |
| 5 | Set Temp. Compensation | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | # | # | Set TC[| | 0 |
| 6 | Set Power Control | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | # | # | Set PC | | 10b |
| 7 | Set Adv. Program Control | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | R | Set APC[F | | N/A |
| ⊢ | (double-byte command) | 0 | 0 | # | # | # | # | # | # | # | # | R=00 | | _ |
| 8 | Set Scroll Line LSB Set Scroll Line MSB | 0 | 0 | 0 | 1 | 0 | 0 | # | # | # | # | Set SL[| | 0 |
| ⊢ | | 0 | 0 | 0 | 1 | 1 | 0 | # | # | # | # | Set SL[| | 0 |
| 9 | Set Row Address LSB Set Row Address MSB | 0 | 0 | 0 | 1 | 1 | 1 | # | # | # | # | Set RA | | 0 |
| ⊢ | Set V _{BIAS} Potentiometer | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | | | |
| 10 | (double-byte command) | ő | ő | # | # | # | # | # | # | # | # | Set PM[| 7:0] | 40H |
| 11 | Set Partial Display Control | ō | 0 | 1 | 0 | 0 | ő | 0 | 1 | 0 | # | Set LC | 2[8] | 0 |
| 12 | Set RAM Address Control | 0 | ō | 1 | 0 | 0 | ō | 1 | # | # | # | Set ACI | | 001b |
| | | ő | ő | 1 | ő | ő | 1 | Ö | Ö | 0 | Ö | | | |
| 13 | Set Fixed Lines | Ö | Ö | # | # | # | # | # | # | # | # | Set (FLT, | FLB} | 0 |
| 14 | Set Line Rate | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | # | # | Set LC[| 4:3] | 10b |
| 15 | Set All-Pixel-ON | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | # | Set DC | [1] | 0 |
| 16 | Set Inverse Display | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 1 | # | Set DC | [0] | 0 |
| 17 | Set Display Enable | 0 | 0 | 1 | 0 | 1 | 0 | 1 | # | # | # | Set DC | 4:2] | 110b |
| 18 | Set LCD Mapping Control | 0 | 0 | 1 | 1 | 0 | 0 | 0 | # | # | # | Set LC[| 2:0] | 0 |
| 19 | Set N-Line Inversion | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | Set NIV | 4.01 | 1DH |
| | | _ | _ | - | - | - | # | # | # | # | # | | | |
| 20 | Set Color Pattern | 0 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | # | Set LC | | 0 (BGR) |
| 21 | Set Color Mode | 0 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | # | # | Set LC[| | 10b |
| 22 | Set COM Scan Function | 0 | 0 | 1 | 1 | 0 | 1 | 1 | # | # | # | Set CSF | | 000b |
| 23 | System Reset | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | System I | | N/A |
| 24 | | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 1 - | 1 | No opera | | N/A |
| 25 | Set Test Control (double-byte command) | 0 | 0 | 1 # | 1 # | 1 # | 0 # | 0 # | 1 # | # | T | For testing Do not | | N/A |
| 26 | Set LCD Bias Ratio | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 0 | # | # | Set BRI | | 11b: 12 |
| | Set LCD Bias Ratio | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | Set bit | 1.0] | 110. 12 |
| 27 | Set COM End | ŏ | ő | l : | # | # | # | # | # | # | # | Set CEN | [6:0] | 159 |
| | | ŏ | ō | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | | | _ |
| 28 | Set Partial Display Start | ŏ | ŏ | - | # | # | # | # | # | # | # | Set DST | [6:0] | 0 |
| 29 | Set Dortiel Display End | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | Cot DEN | re.01 | 159 |
| 29 | Set Partial Display End | 0 | 0 | - | # | # | # | # | # | # | # | Set DEN | [0:0] | 159 |
| 30 | Set Window Program | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | | Set | 0 |
| - | Starting Column Address | 0 | 0 | - | # | # | # | # | # | # | # | | WPC0 | Ů |
| 31 | Set Window Program | 0 | 0 | 1 | 1 " | 1 | 1 | 0 | 1 " | 0 | 1 | Shared | Set | 0 |
| - | Starting Row Address | 0 | 0 | # | # | # | # | # | # | # | # | with MTP | WPP0 | |
| 32 | Set Window Program Ending Column Address | 0 | 0 | 1 | 1 # | 1 # | 1 # | 0 | 1 # | 1 # | 0 # | commands | Set WPC1 | 127 |
| ⊢ | Set Window Program | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | Set | |
| 33 | Ending Row Address | ő | 0 | # | # | # | # | # | # | # | # | | WPP1 | 159 |
| 34 | Window Program Mode | ō | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | # | Set AC | | 0: Inside |
| | | ő | ō | 1 | Ö | 1 | 1 | 1 | 0 | ő | 0 | | | |
| 35 | Set MTP Operation control | ŏ | ŏ | | - | : | # | # | # | # | # | Set MTP | C[4:0] | 10H |
| | | 0/5 | 1044E | | | - | - | | - | | - | 8 -41 - | _ | D-514 |
| | Command | C/D | W/R | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Actio | n | Default |
| | | 0 | 0 | 1 | 0 | 1 # | 1 # | 1 # | 0 | 0 | 1 # | Set MTPN | / [6:0] | _ |
| | C-+ NATO MA-t NAI | | _ ^ | | | | | | # | # | | | | 0 |
| 36 | Set MTP Write Mask | 0 | 0 | - | # | # | # | " | | | | MTPM1 | [1:0] | |
| 36 | Set MTP Write Mask | 0 | 0 | - | - | - | - | - | - | # | # | MTPM1 | | |
| | | 0 | 0 | 1 | 1 | 1 | 1 | - 0 | 1 | # 0 | # 0 | MTPM1 | Set | N/A |
| | | 0 0 0 | 0 0 0 | - 1 # | - 1 # | - 1 # | 1 # | - 0 # | 1 # | # 0 # | # 0 # | | Set MTP1 | N/A |
| | | 0 0 0 | 0 0 0 | 1 # | - 1 # 1 | 1 # | 1 # | - 0 # 0 | 1 # | # 0 # 0 | # 0 # 1 | Shared with | Set MTP1 Set | N/A N/A |
| 37 | Set V _{MTP1} Potentiometer | 0 0 0 0 0 | 0 0 0 | - 1 # 1 # | 1 # 1 # | 1 # 1 # | 1 # 1 # | - 0 # 0 # | 1 # 1 # | # 0 # 0 # | # 0 # 1 | Shared with Window | Set MTP1 Set MTP2 | |
| 37 | Set V _{MTP1} Potentiometer | 0 0 0 0 0 0 | 0 0 0 0 0 | 1 # 1 # 1 | - 1 # 1 # | 1 # 1 # | 1 # 1 # | - 0 # 0 # | 1 # 1 # 1 | # 0 # 0 # | # 0 # 1 # | Shared with Window Program | Set MTP1 Set MTP2 Set | |
| 37 | Set V _{MTP1} Potentiometer Set V _{MTP2} Potentiometer | 0 0 0 0 0 0 0 | 0 0 0 0 0 | 1 # 1 # | - 1 # 1 # | - 1 # 1 # | - 1 # 1 # | - 0 # 0 # | 1 # 1 # | # 0 # 0 # 1 | # 0 # 1 # 0 # | Shared with Window | Set MTP1 Set MTP2 Set MTP3 | N/A |
| 37 | Set V _{MTP1} Potentiometer Set V _{MTP2} Potentiometer | 0 0 0 0 0 0 | 0 0 0 0 0 | 1 # 1 # 1 | - 1 # 1 # | 1 # 1 # | 1 # 1 # | - 0 # 0 # | 1 # 1 # 1 | # 0 # 0 # | # 0 # 1 # | Shared with Window Program | Set MTP1 Set MTP2 Set | N/A |

10.QUALITY SPECIFICATIONS

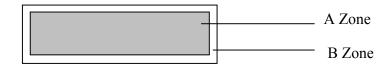
10.1 Standard of the product appearance test

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 30 cm or more.

Viewing direction for inspection is 45° from vertical against LCM.



Definition of zone:



A Zone: Active display area (minimum viewing area). B Zone: Non-active display area (outside viewing area).

10.2 Specification of quality assurance

AQL inspection standard

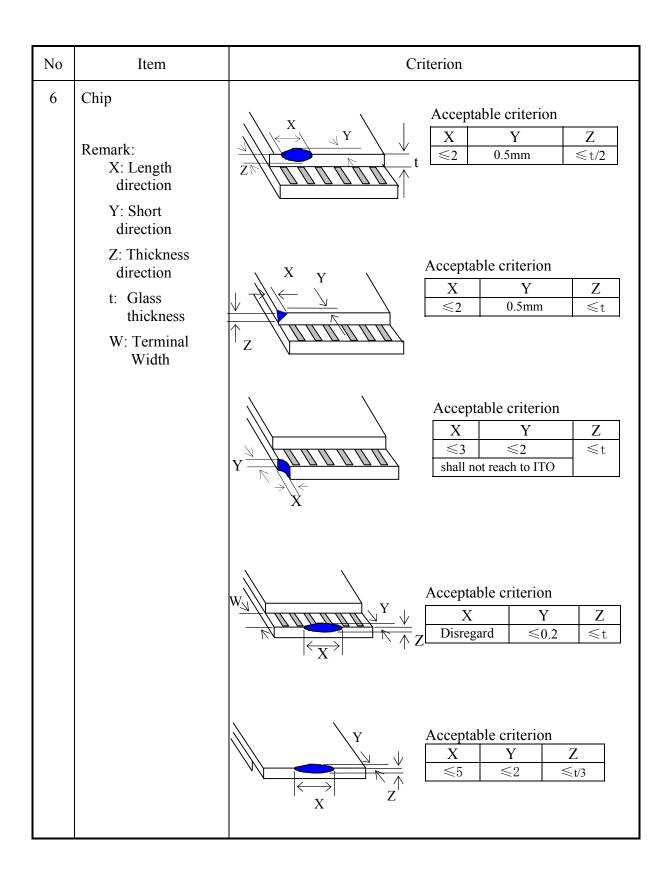
Sampling method: MIL-STD-105E, Level II, single sampling

Defect classification (Note: * is not including)

| Classify | | Note | AQL | |
|----------|---------------|------------------------------|-----|------|
| Major | Display state | Short or open circuit | 1 | 0.65 |
| | | LC leakage | | |
| | | Flickering | | |
| | | No display | | |
| | | Wrong viewing direction | | |
| | | Contrast defect (dim, ghost) | 2 | |
| | | Back-light | 1,8 | |
| | Non-display | Flat cable or pin reverse | 10 | |
| | | Wrong or missing component | 11 | |
| Minor | Display | Background color deviation | 2 | 1.0 |
| | state | Black spot and dust | 3 | |
| | | Line defect, Scratch | 4 | |
| | | Rainbow | 5 | |
| | | Chip | 6 | |
| | | Pin hole | 7 | |
| | | Protruded | 12 | |
| | Polarizer | Bubble and foreign material | 3 | |
| | Soldering | Poor connection | 9 | |
| | Wire | Poor connection | 10 | |
| | TAB | Position, Bonding strength | 13 | |

Note on defect classification

| No. | Item | Criterion |
|-----|---|---|
| 1 | Short or open circuit | Not allow |
| | LC leakage | |
| | Flickering | |
| | No display | |
| | Wrong viewing direction | |
| | Wrong Back-light | |
| 2 | Contrast defect | Refer to approval sample |
| | Background color deviation | |
| 3 | Point defect, Black spot, dust (including Polarizer) $\phi = (X+Y)/2$ | Point Size $\begin{array}{c cccc} & \text{Point} & \text{Acceptable Qty.} \\ \hline & & & & \\ \hline & & \\$ |
| 4 | Line defect, Scratch | $\begin{array}{c cccc} & & & & & & \\ & & & & & \\ & & & & \\ L & & & &$ |
| 5 | Rainbow | Not more than two color changes across the viewing area. |



| No. | Item | Criterion | | |
|-----|---|---|--|--|
| 7 | Segment pattern $W = \text{Segment width}$ $\phi = (X+Y)/2$ | (1) Pin hole $\phi < 0.10 \text{mm is acceptable.}$ Y | | |
| 8 | Back-light | (1) The color of backlight should correspond its specification.(2) Not allow flickering | | |
| 9 | Soldering | (1) Not allow heavy dirty and solder ball on PCB. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land. Land Land 50% lead | | |
| 10 | Wire | (1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable. | | |
| 11* | PCB | (1) Not allow screw rust or damage. (2) Not allow missing or wrong putting of component. | | |

| No | Item | Criterion | |
|----|--------------------------------|---|--|
| 12 | Protruded W: Terminal Width | Acceptable criteria: $Y \le 0.4$ | |
| 13 | TAB | 1. Position $\begin{array}{cccccccccccccccccccccccccccccccccccc$ | |
| | | 2 TAB bonding strength test TAB P (=F/TAB bonding width) ≥650gf/cm ,(speed rate: 1mm/min) 5pcs per SOA (shipment) | |
| 14 | Total no. of acceptable Defect | A. Zone Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm B. Zone It is acceptable when it is no trouble for quality and assembly in customer's end product. | |

10.3 Reliability of LCM

| No. | Test Item | Test Condition | Inspection after test |
|-----|----------------------------|--|--|
| 1 | High Temperature Storage | 80±2°C/72 hours | |
| 2 | Low Temperature Storage | -30±2°C/72 hours | Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD; 2.Sealleak; 3.Non-display; 4.missing segments; 5.Glass crack; 6.Current Idd is twice higher than initial value. |
| 3 | High Temperature Operating | 75±2°C/48 hours | |
| 4 | Low Temperature Operating | -25±2°C/48 hours | |
| 5 | Temperature Cycle | -25±2°C~25~75±2°C×10cycles (30min.) (5min.) (30min.) | |
| 6 | Damp Proof Test | 50°C±5°C×90%RH/48 hours | |
| 7 | Vibration Test | Frequency: 10Hz~55Hz~10Hz Amplitude: 1.5mm, X, Y, Z direction for total 3hours (Packing condition) | |
| 8 | Dropping test | Drop to the ground from 1m height, one time, every side of carton. (Packing condition) | |
| 9 | ESD test | Voltage:±16KV, Air discharge, 10time*5dots | |

Remark:

- 1. The test samples should be applied to only one test item.
- 2. Sample size for each test item is 5~10pcs.
- 3. For Damp Proof Test, Pure water(Resistance \geq 10M Ω) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part. Using ionizer(an antistatic blower) is recommended at working area in order to reduce electro-static voltage. When removing protection film from LCM panel, peel off the tag slowly(recommended more than one second) while blowing with ionizer toward the peeling face to minimize ESD which may damage electrical circuit.
- 5.EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6.Please use automatic switch menu(or roll menu) testing mode when test operating mode.

10.4 Precaution for using LCD/ICM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

General Precautions:

- 1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
- 2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isoproply alcohol, ethyl alcohol or trichlorotriflorothane, do not use water, ketone or aromatics and never scrub hard.
- 3. Do not tamper in any way with the tabs on the metal frame.
- 4. Do not made any modification on the PCB without consulting HES.
- 5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
- 6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
- 7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

Static Electricity Precautions:

- 1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
- 2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
- 3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
- 4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
- 5. Only properly grounded soldering irons should be used.
- 6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
- 7. The normal static prevention measures should be observed for work clothes and working benches.
- 8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

Soldering Precautions:

- 1. Soldering should be performed only on the I/O terminals.
- 2. Use soldering irons with proper grounding and no leakage.
- 3. Soldering temperature: 280°C±10°C
- 4. Soldering time: 3 to 4 second.
- 5. Use eutectic solder with resin flux filling.
- 6. If flux is used, the LCD surface should be protected to avoid spattering flux.
- 7. Flux residue should be removed.

Operation Precautions:

- 1. The viewing angle can be adjusted by varying the LCD driving voltage Vo.
- 2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
- 3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
- 4. Response time increases with decrease in temperature.
- 5. Display color may be affected at temperatures above its operational range.
- 6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
- 7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.

Limited Warranty

HES LCDs and modules are not consumer products, but may be incorporated by HES's customers into consumer products or components thereof, HES does not warrant that its LCDs and components are fit for any such particular purpose.

- 1. The liability of HES is limited to repair or replacement on the terms set forth below. HES will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between HES and the customer, HES will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with HES general LCD inspection standard. (Copies available on request)
- 2. No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.
- 3. In returning the LCD/LCM, they must be properly packaged; there should be detailed description of the failures or defect.